

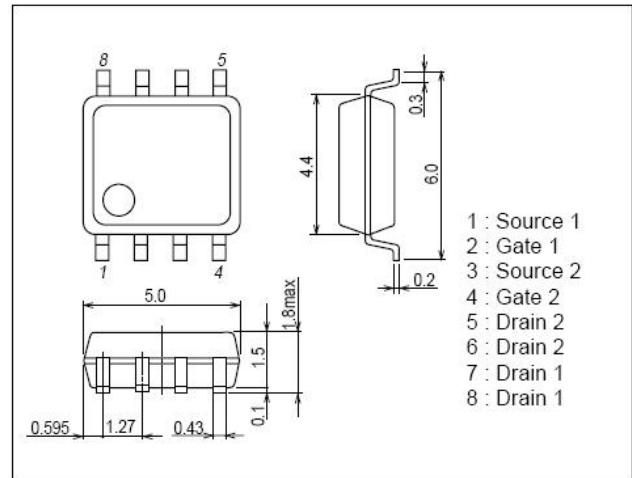
Features

- Low On resistance.
- 4.5V drive.
- RoHS compliant.



Package Dimensions

unit : mm
SOP-8



Specifications

Absolute Maximum Ratings at $T_a=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		30	V
Gate-to-Source Voltage	V_{GSS}		± 20	V
Drain Current (DC)	I_D		12	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	48	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board ($1000\text{mm}^2 \times 0.8\text{mm}$) 1unit	2	W
Total Dissipation	P_T	Mounted on a ceramic board ($1000\text{mm}^2 \times 0.8\text{mm}$)	1.28	W
Channel Temperature	T_{ch}		150	$^{\circ}\text{C}$
Storage Temperature	T_{stg}		-55~+150	$^{\circ}\text{C}$

Electrical Characteristics at $T_a=25^{\circ}\text{C}$

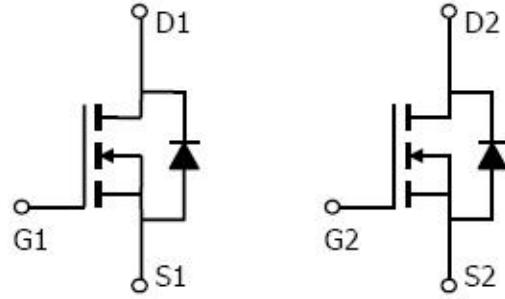
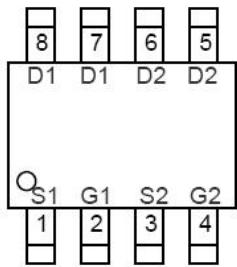
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}$, $I_D = 250\text{A}$	30	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$	-	-	1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	1.3	1.7	2.1	V
Static Drain-to-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 10\text{V}$, $I_D = 12\text{A}$	-	9.5	11	$\text{m}\Omega$
	$R_{DS(ON)}$	$V_{GS} = 4.5\text{V}$, $I_D = 9\text{A}$	-	14.5	18.5	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	-	828	-	pF
Output Capacitance	C_{oss}	$V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	-	196	-	pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	-	174	-	pF

Si4812

Electrical Characteristics at $T_a=25^{\circ}\text{C}$ (Continued)

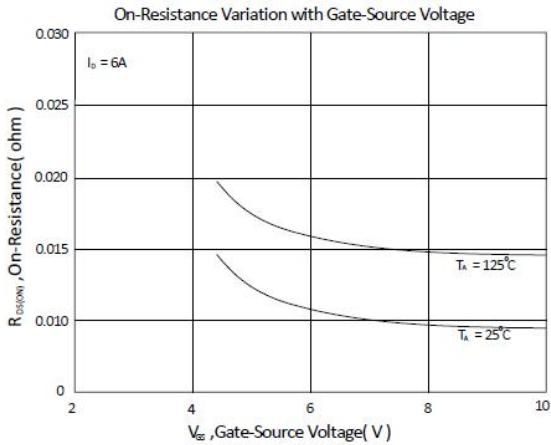
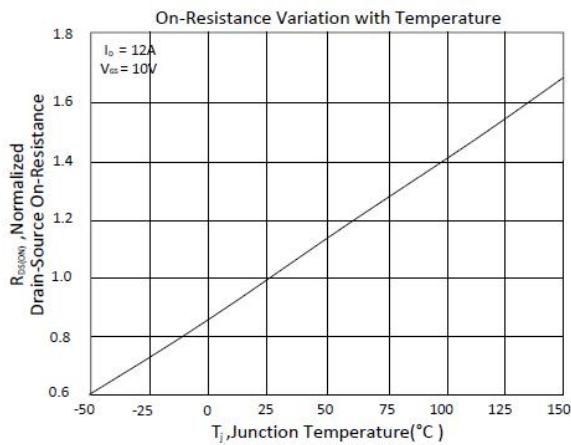
Parameter	Symbol	Conditions	Ratings			Unit
			min	Typ	max	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{DS}=15\text{V}, I_D = 1\text{A}, V_{GS} = 10\text{V}, R_{GS} = 6\Omega$	-	12	-	nS
Rise Time	t_r		-	10	-	nS
Turn-off Delay Time	$t_{d(\text{off})}$		-	30	-	nS
Fall Time	t_f		-	15	-	nS
Total Gate Charge	Q_g	$V_{DS}=15\text{V}, V_{GS}=10\text{V}, I_D=12\text{A}$	-	17.6	-	nC
Gate-to-Source Charge	Q_{gs}		-	2.8	-	nC
Gate-to-Drain "Miller" Charge	Q_{gd}		-	7.4	-	nC
Diode Forward Voltage	V_{SD}	$I_S=1\text{A}, V_{GS}=0\text{V}$	-		1.2	V

Pin Description

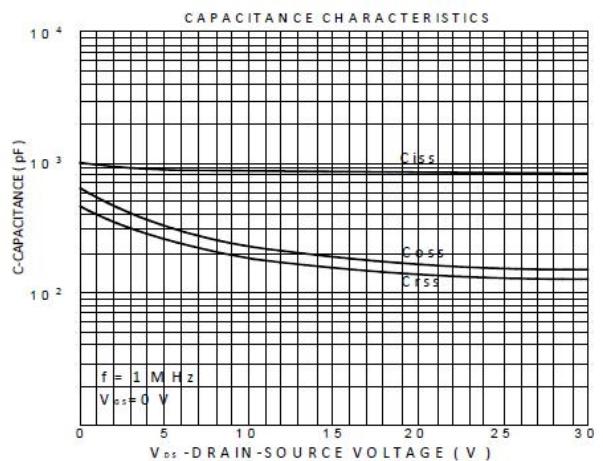
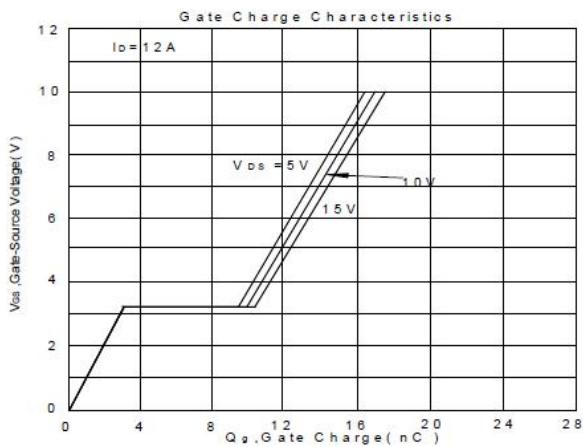
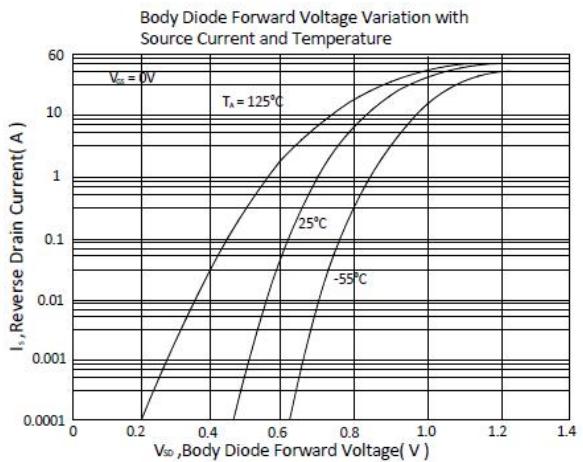
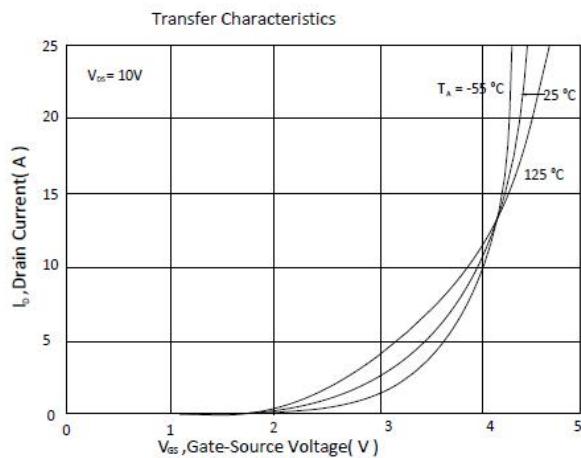
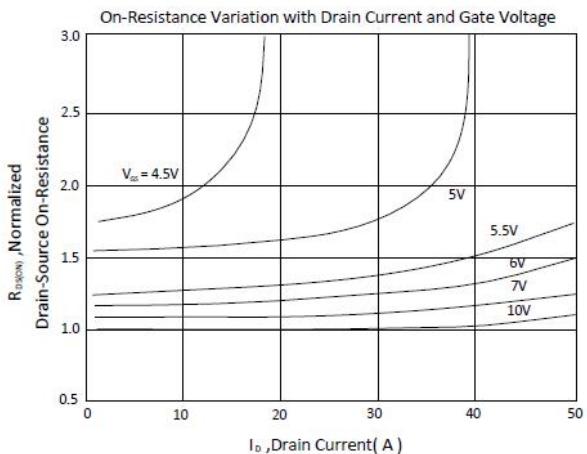
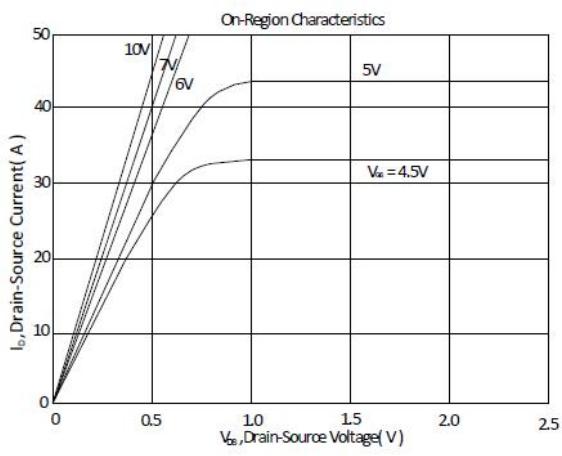


TOP VIEW
SOP-8

Typical Characteristics at $T_a=25^{\circ}\text{C}$



Si4812



Si4812

